

**FIG. 1**

FIG. 2 is a block diagram of a receiver system 62 according to one embodiment of the present invention.

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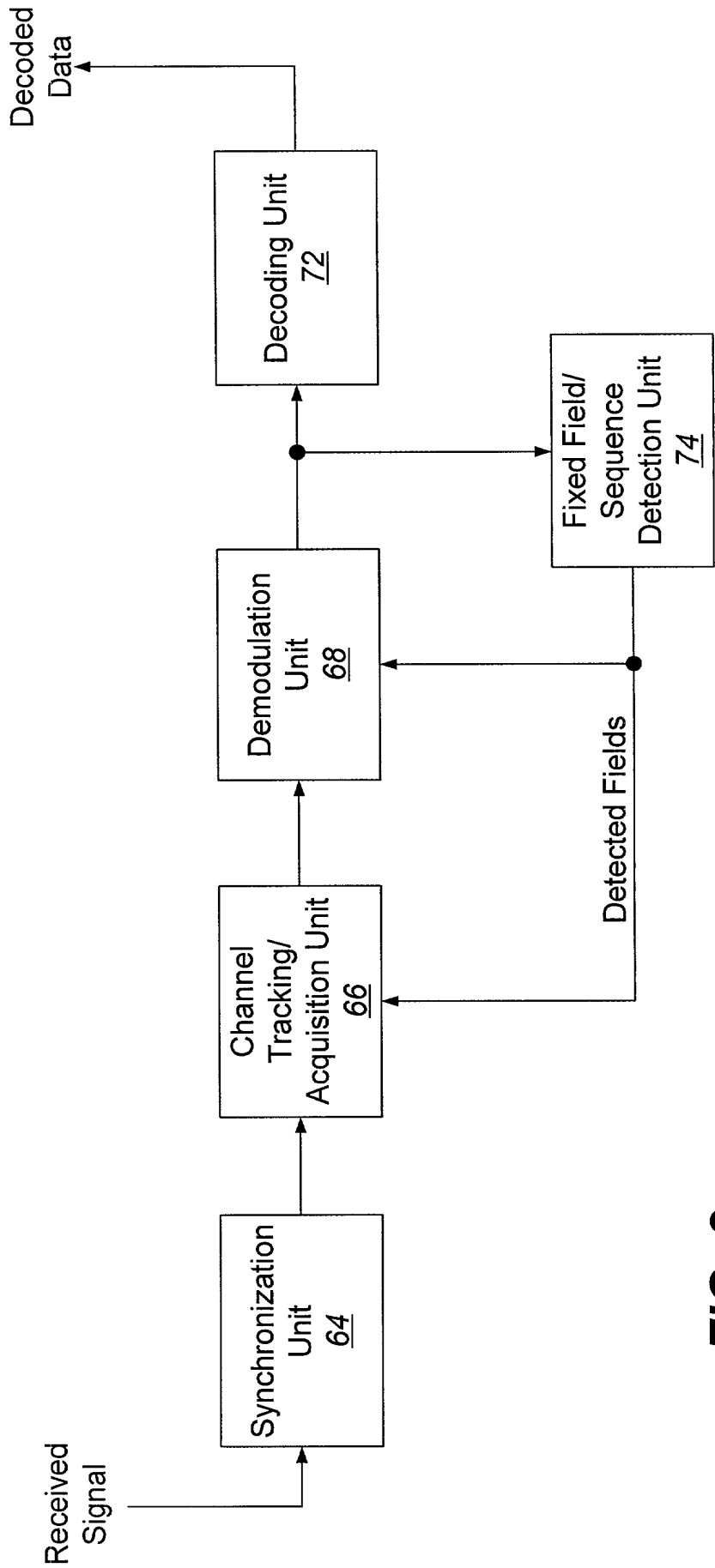


FIG. 2

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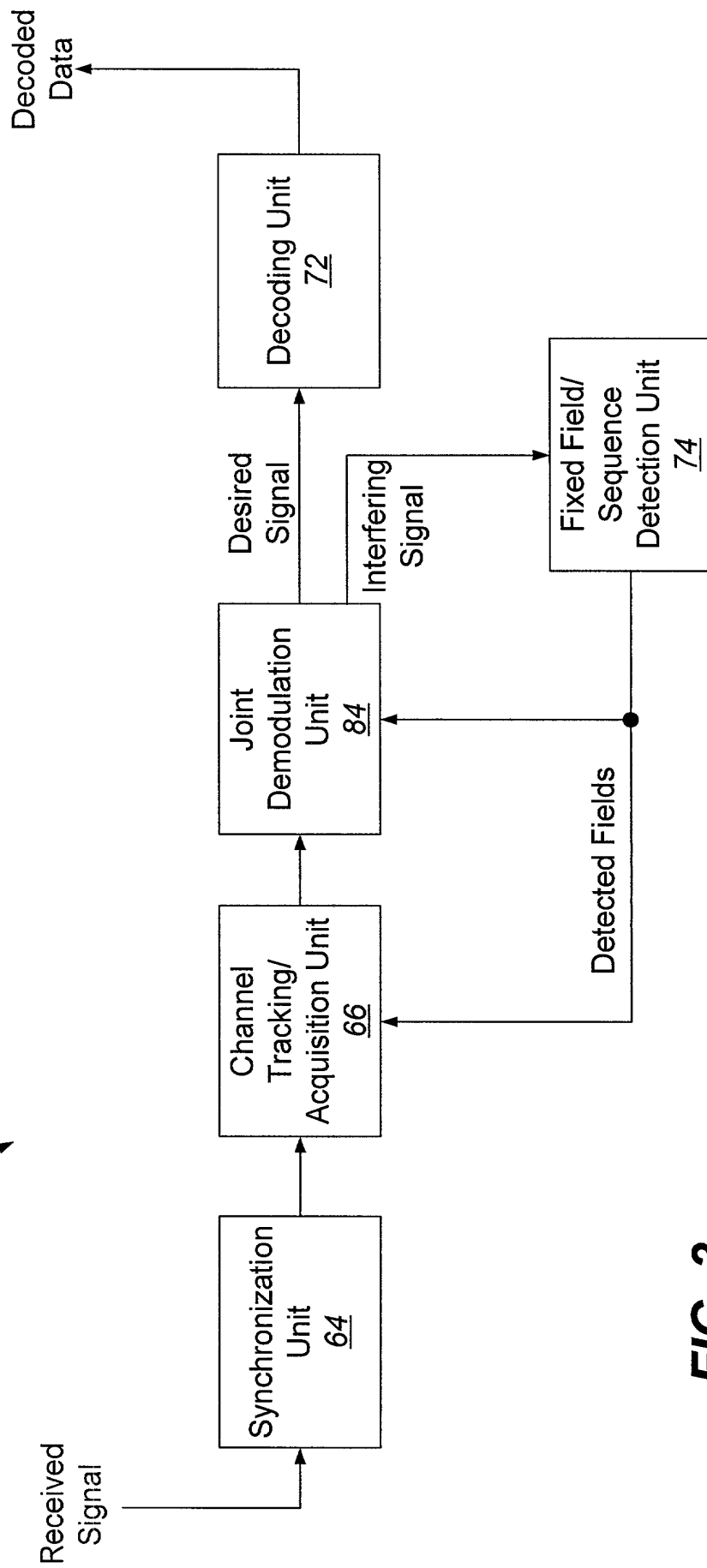
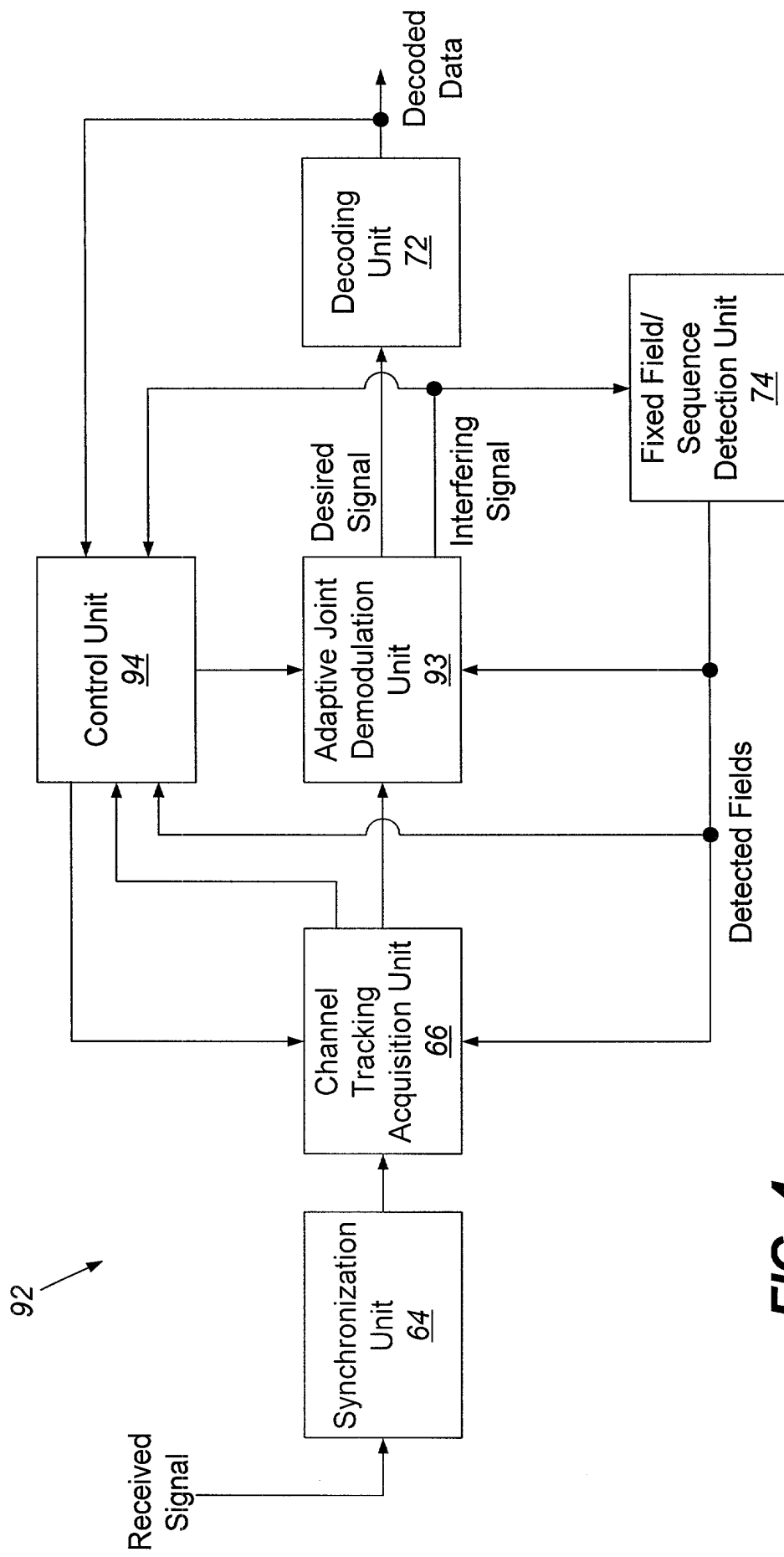
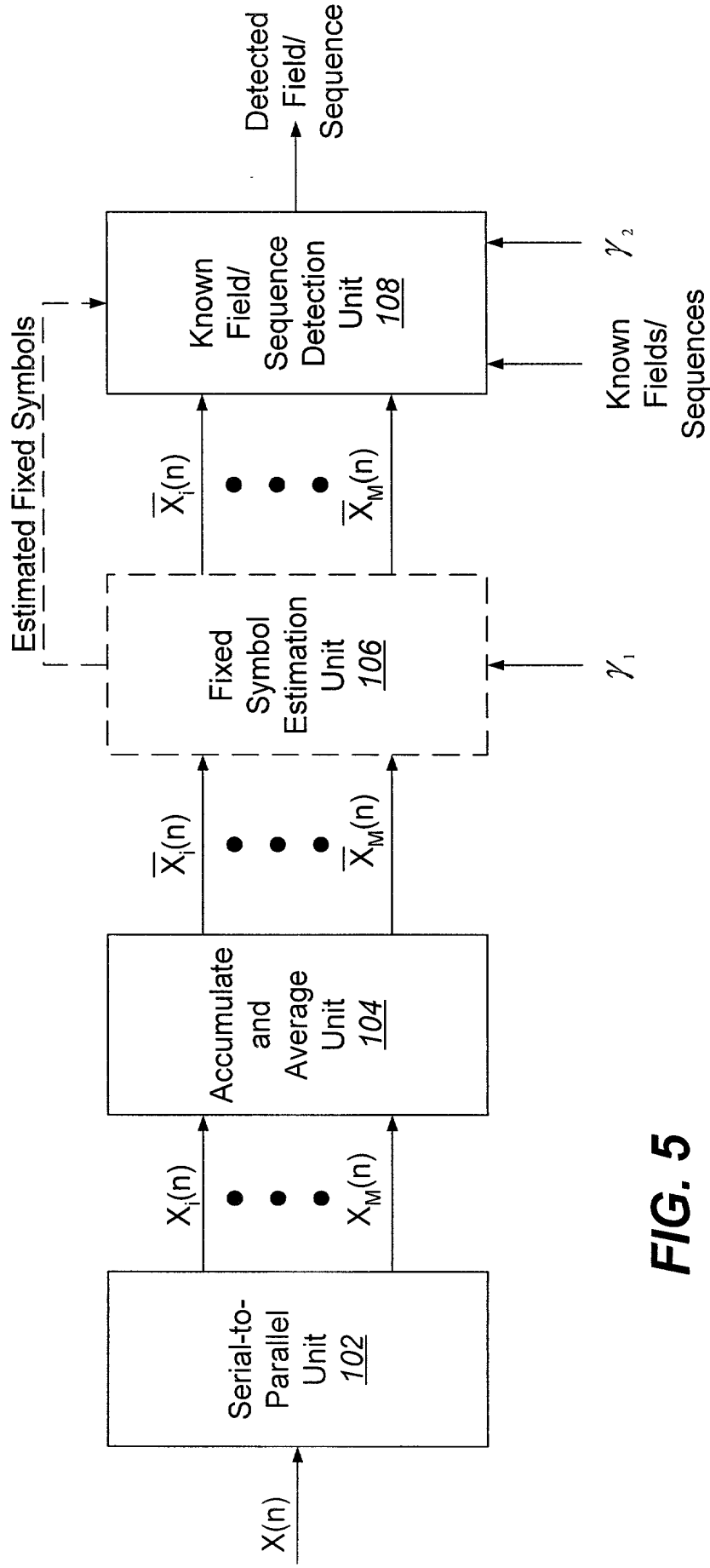


FIG. 3

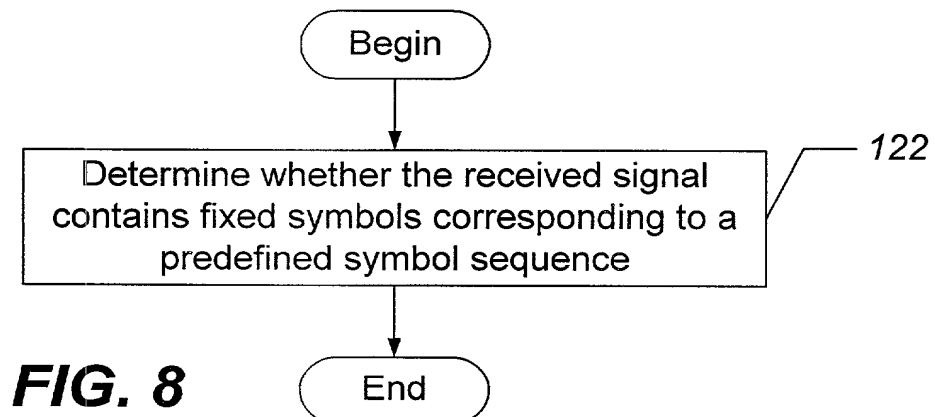
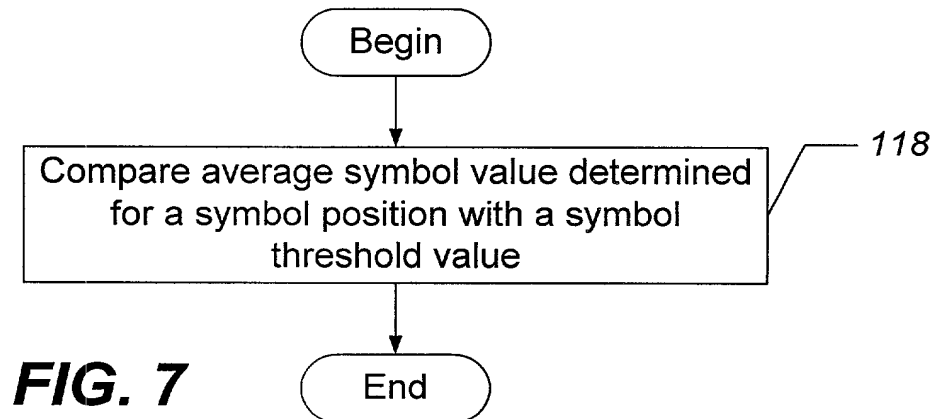
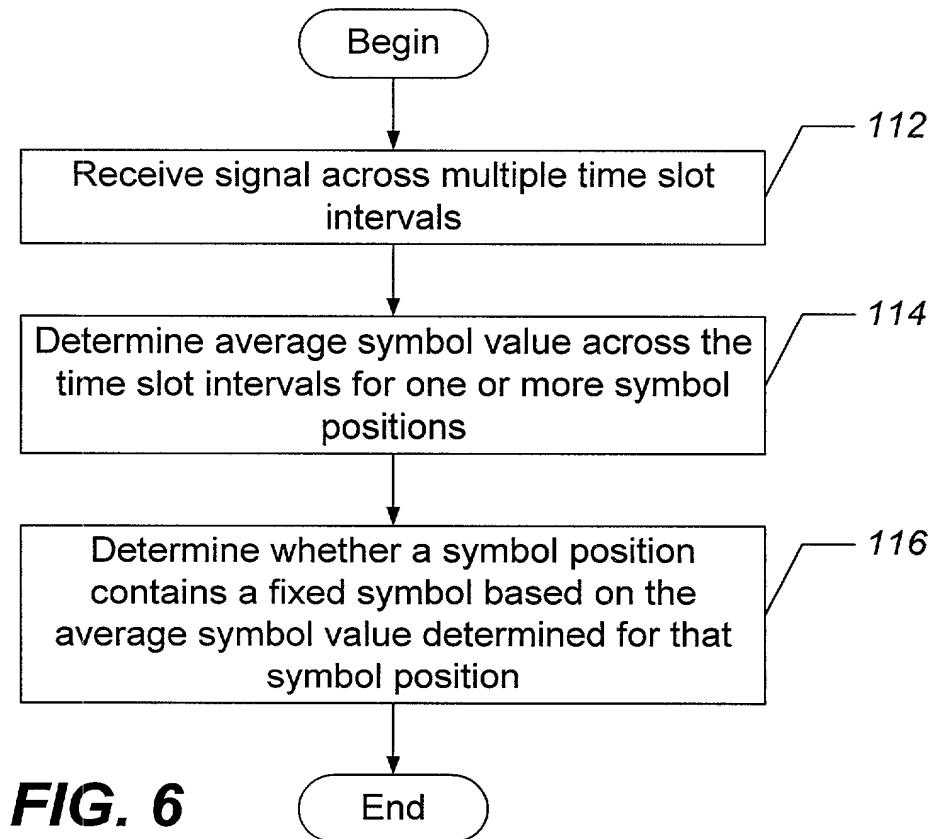


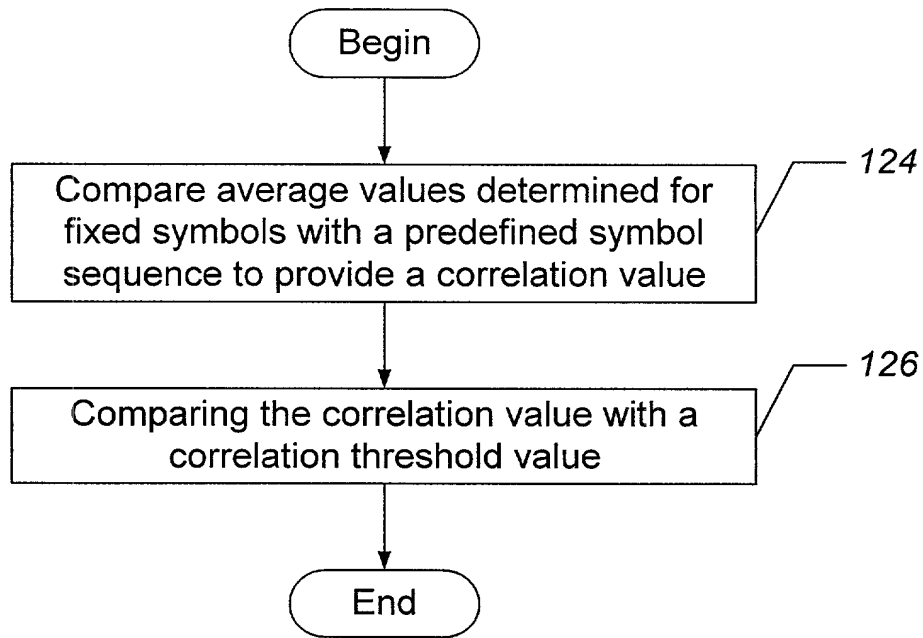
**FIG. 4**

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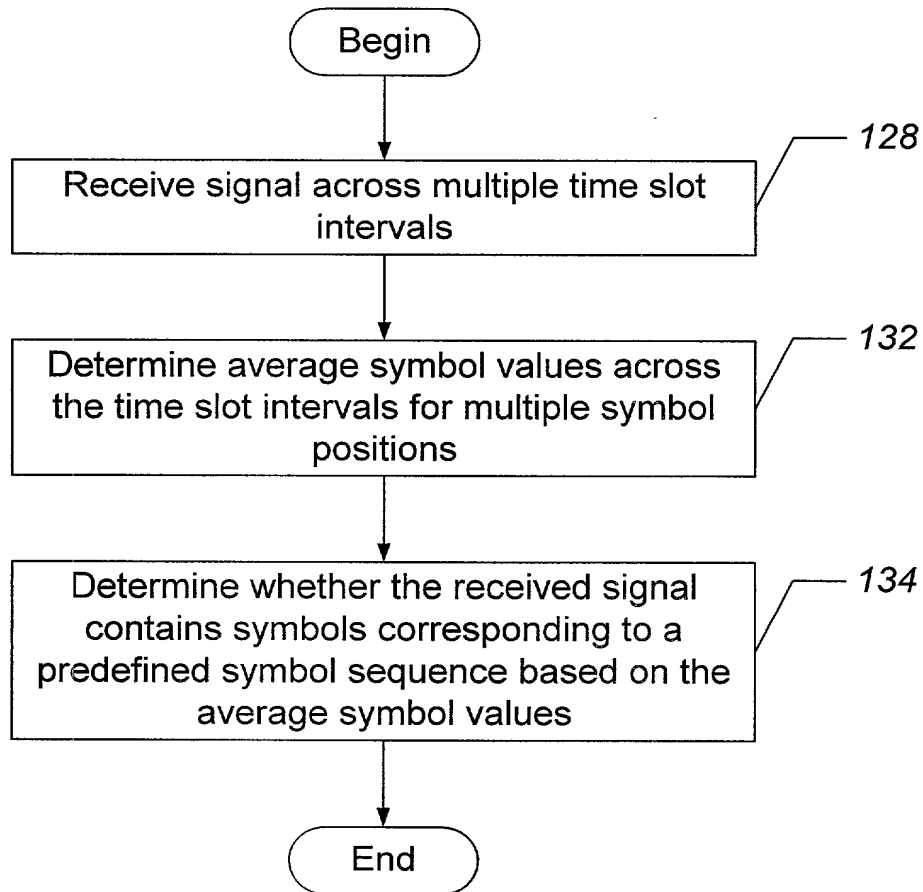


**FIG. 5**

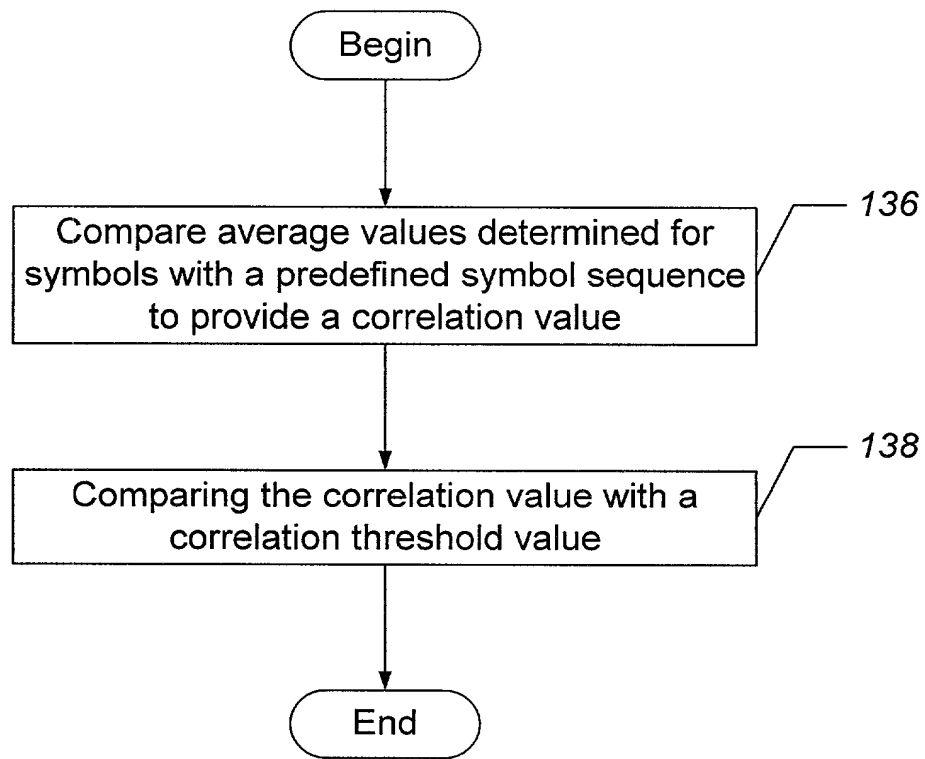




**FIG. 9**



**FIG. 10**



**FIG. 11**